CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	Assume that a 4-bit serial in/serial out shift register is initially clear. We wish to store the nibble 1100. What will be the 4-bit pattern after the second clock pulse?	A. 1100 B. 0011 C. 0000 D. 1111
2	5-bit Johnson counter sequences through _____ states	A. 7 B. 10 C. 32 D. 25
3	The best state assignment tends to _____.	A. Maximizes the number of state variables that don't change in a group of related states B. Minimizes the number of state variables that don"t change in a group of related states C. Minimize the equivalent states D. None of given options
4	_____ is used to minimize the possible no. of states of a circuit.	A. State assignment B. State reduction C. Next state table D. State diagram
5	In a state diagram, the transition from a current state to the next state is determined by.	A. Current state and the inputs B. Current state and outputs C. Previous state and inputs D. Previous state and outputs
6	_____ is one of the examples of asynchronous inputs.	A. J-K input B. S-R input C. D input D. Clear Input (CLR)
7	_____ is one of the examples of synchronous inputs	A. J-K input B. EN input C. Preset input (PRE) D. Clear Input (CLR)
8	The PROM consists of a fixed non-programmable _____ Gate array configured as a decoder	A. AND B. OR C. NOT D. XOR
9	_____ is invalid number of cells in a single group formed by the adjacent cells in K-map	A. 2 B. 8 C. 12 D. 16
10	The output of the expression F=A.B.C will be Logic _____ when A=1, B=0, C=1	A. Undefined B. One C. Zero D. No Output as input is invalid