

PPSC Computer Science Full Book MCQ's test

| Sr | Questions | Answers Choice |
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| 1 | Which formula is used to calculate the number of write stall cycles. | A. Reads * read miss rate* read miss penalty B. write * write miss + Write buffer stalls C. Memory access* cache miss rate * cache miss penalty D. None |
| 2 | What section of the config Web file is used for storing a slist of authorized users. | A. Authorization B. Authentication C. Authentication D. None |
| 3 | Which of the following is not a type of page margin. | A. Left B. Right C. Center D. Top |
| 4 | This is the stage in Data base design where one Gathers and list all the necessary field for the data base project. | A. Data definition B. Data refinement C. Establishing Relationship D. None of the above |
| 5 | In microprocessor one of the operands holds a special register called. | A. Calculator B. Dedicated C. Accumulator D. None |
| 6 | Which of the following is not the style for the font. | A. Shadow B. Outline C. Carve D. Bold |
| 7 | Which of the following is part of system testing. | A. Business process based testing B. Performance load and stress testing C. Usability testing D. Top down integration testing |
| 8 | Which of the following are variables that represent the data elements in a managed object. | A. Notifications B. Attributes C. Operations D. Sub routines |
| 9 | The object refers to the 3D representation through linear circular or some other representation are called. | A. Quadric surface B. Sweep representation C. Torus D. None |
| 10 | are the three dimensional analogs of Quaid trees | A. quadric B. Octrees C. Geometry D. None |
| 11 | A small button with three dots usually displayed at the right of field properties box. | A. Make button B. Expression button C. Build button D. None |
| 12 | The register can be divided are. | A. 3 B. 4 C. 5 D. 6 |
| 13 | A computer consists of. | A. A central processing unit B. A memory C. Input and output unit D. All of the above |
| 14 | The lower red curvy arrow show that CPU places the address extracted from the memory location on the. | A. Address bus B. System bus C. Control bus D. Data bus |
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| 15 | occurs during the construction stage of development. | A. Plan B. Design C. Coding D. Actual testing |
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| 16 | They were fabricated using a low powered version of the HMOS technology called. | A. HSMOS B. HCMOS C. HSSOM D. None |
| 17 | Which is the following is not a storage class in C. | A. Auto B. Struct C. Extern D. Static |
| 18 | If you make an invalid entry in the input mask wizard dialog box the will display to advise you that the entry is not coorect. | A. Text eooro B. Validation error C. Literal error D. Entry error |
| 19 | is a black box testing technique appropriate to all levels of testing. | A. Acceptance testing B. Regression testing C. Fulvalene partitioning D. Quality assurance |
| 20 | Testing is the used to determine the the solution solves the problem. | A. Data B. Validity C. Technique D. Problem |