PPSC Computer Science Full Book test

Sr	Questions	Answers Choice
1	Which year intel family ensured.	A. 1965 B. 1981 C. 1978 D. 1999
2	How many speed of 8088 , 8085 , 8086 microprocessor.	A. 2.5 million instructions per second B. 1.5 Million instruction per second C. 3.5 Million instruction second D. 1.6 Million instruction per second
3	Which is 16 bit microprocessor .	A. 8088 B. 8086 C. 8085 D. All of these
4	Which is the main feature of 8085	A. Internal clock generator B. Internal system controller C. Higher clock frequency D. All of these
5	How many bit microprocessor developed by intel.	A. 4 bit B. 8 bit C. 32 bit D. 64 bit
6	In 1977 which corporation introduce an updated version of the 8080- the 8085	A. Motorola B. Intel C. National D. Rock well
7	Which year intel corporation introduced an updated version of the 8080-the 8085	A. 1965 B. 1977 C. 1976 D. 1985
8	How many stages has fetch execute cycle.	A. 3 B. 4 C. 5 D. 6
9	Which is the microprocessor launch by Fairchild company	A. F-6 B. F-8 C. Both a and b D. None
10	Motorola has declined from having nearly _____ share of the microprocessor market to much smaller share.	A. 30% B. 40% C. 50% D. 60%
11	How many bit MC6800 mlcroprocessor.	A. 4-bit B. 8-bit C. 16 -bit D. 32-bit
12	Which is the microprocessor launched by Motorola corporation introduced.	A. Mc6800 B. 8080 C. IMP-8 D. RPS-8
13	The _____ of can assembly line to be 1/tp.	A. Clock period B. Pipelining C. Through out D. Flow thorugh
14	Who is the determined by the time taken by the stages the requires the most processing time.	A. Clock period B. Flow through C. Through out D. None
15	T1 is the time taken for the stage and there are n stages in the.	A. Through out B. Assembly line C. Both a and b D. None

16	The clock period is denoted by	A. Tp B. T1+T2+T3.....................+ Tn C. P1 D. None
17	The time taken for all stages of the assembly line to become active is called the	A. Flow through time B. Clock period C. Through put D. All of these
18	The fetch execute cycle is to use a system know as.	A. Assembly line B. Pipelining C. Cache D. None
19	_____ Memory system which is discussed later can improve matters in this respect.	A. Data memory B. Cache memory C. Memory D. None
20	Which process information at a much faster rate than it can retrieve it from memory	A. ALU B. Processor C. Micro procesor D. CPU
21	Who is the represents the fundamental process in the operation of the CPU	A. The fetch execute cycle and pipelining. B. The assembly C. Both a and b D. None
22	How can we make computers work faster.	A. the fetch execute cycle and pipelining B. The assembly C. Both a and b D. None
23	The range of this rating for which microprocessor of.	A. VLSI B. Motorola C. Intel D. Zilog
24	Which is the most commonly measured in term of MIPS previously millions instructing peer second.	A. Microprocesser B. Performance of a microprocessor. C. Assembly line D. None
25	the growth of LSI technology on	A. 1994-1995 B. 1971-1977 C. 1972-1978 D. None
26	The growth of SSI up to.	A. 100 device on a chip B. 200 device on a chip C. 300 device on a chip D. 400 device on a chip
27	The growth of medium scale integration in.	A. Till 1971 B. Till 1970 C. Till 1972 D. Till 1969
28	How are the growth of SSI technology in.	A. 1956 on words B. 1965 on words C. 1976 on words D. 1978 on words
29	The growth of transistor technology in	A. 1946-1967 B. 1958-1964 C. 1985-1999 D. None
30	The growth of vacuum tube technology has been listed as follow.	A. 1946-1957 B. 1958-1964 C. 1985-1999 D. None