PPSC Computer Science Full Book test

Sr	Questions	Answers Choice
1	Who is the Brian of computer	A. ALU B. CPU C. MU D. None
2	PC's use_____ based on this architecture.	A. CPU B. ALU C. MU D. None
3	CISC stand for	A. Complex instruction set computer B. Computer instruct set of computer C. Compared instruction set computer D. None
4	Which are the architectural paradigms in microprocessor.	A. RISC B. CISC C. PISC D. A and B
5	How many architectural paradigms in microprocessor.	A. 2 B. 3 C. 4 D. 6
6	DEC stand for	A. Digital electronic computer B. Digital electronic corporation C. Digital equipment corporation D. None
7	RISC stand for.	A. Reduced instruction set computer B. Reduced instruct set compare C. Reduce instruction stand computer D. All of these
8	ISA stand for	A. Instruct set area B. Instruction set architecture C. Both a and b D. None
9	Which is not the open source OS.	A. Debian B. BSD unix C. Gentoo and Red Hat Linux D. Windows
10	Which is not the main feature of DEC alpha	A. 64 bit RISC processor B. Designed to replace 32 VAX C. Seven stage split integer point pipeline D. Variable instruction length
11	Alpha AXP is developed by	A. DEC B. IBM C. Motorola D. Intel
12	Which is not the main architectural feature of Power PC.	A. It is not based on RISC B. Superscalar implementation C. Both 32 and 64 bit D. Paged memory managemnet architecture
13	Power PC microprocessor architecture is developed by.	A. Apple B. IBM C. Motorola D. All these
14	What is the maximum clock speed of PIII processor.	A. 1.0 GHz B. 1.1 GHz C. 1.2 GHz D. 1.3 GHz

15	Pentium III processor is released in the form of.	A. Socket 370 version B. Slot 1 version in plastic cartridge C. Both a and b D. None
16	Which is the professional or Business version of intel processor.	A. Pentium II B. Pentium pro C. Pentium MMX D. Pentium Xeon
17	Pentium pro can address_____ of memory.	A. 4 GB B. 128 GB C. 256 GB D. 512 GB
18	L2 cache memory is places at _____	A. On processor B. On mother board C. On memory D. All of these
19	L1 cache memory is places at	A. On processor B. On mother board C. On memory D. All of these
20	Pentium pro processor contains	A. L1 Cache B. L2 cache C. Both a and b D. None
21	VGA stands for	A. Visual graph area B. Visual graphics array C. Visual graph accept D. All of these
22	GUI stand for	A. Graphical user interface B. Graph used intel C. Graphical use inter D. None
23	CAD stand for.	A. Computer aided drafting B. Compare aided drafting C. Both a and b D. None
24	Which is the microprocessor launched by Zilog.	A. Z-2 B. Z-4 C. Z-8 D. Z-6
25	Which is the microprocessor launched by Rock well international.	A. RPS-4 B. RPS-8 C. RPS-8 D. All of these
26	Which is the microprocessor. launched by national semiconductor.	A. IMP-4 B. IMP-8 C. IMP-6 D. IMP-7
27	Which is the microprocessor launched by intel	A. z-8 B. 8080 C. 8000 D. None
28	Who was introduce the 80286 micro processor updated on 80086 in 1983.	A. Motorola B. Fairchild C. Intel D. all of above
29	Which processor provided 1 MB memory.	A. 16-bit 8086 and 8088 B. 32-bit 8086 and 8088 C. 64 bit 8086 and 8088 D. 8 bit 8086 and 8088
30	Which corporation decided to use 8088 microprocessor in personal computer.	A. IBM B. CRT C. PMN D. SPS