

## PPSC Computer Science Full Book MCQ's test

| Sr | Questions   | Answers Choice  |
|----|---|---|
| 1  | As computer to diskettes the hard disks are   | A. More expensive<br>B. More portable<br>C. Less rigid<br>D. Slowly accessed  |
| 2  | What it is called to present information in a particular order based on numeric or alphabetical value.  | A. Sorting<br>B. Searching<br>C. Arranging<br>D. cropping   |
| 3  | All the abbreviations is vi editor can be shown by  | A. All<br>B. ab<br>C. atat<br>D. ashow  |
| 4  | In which transformation the mirror image of an object can be seen with respect to x-axis ,y-axis , z-axis as well as with respect to an arbitrary line. | A. Reflection<br>B. Shearing<br>C. Translation<br>D. None   |
| 5  | An entity class is a class that   | A. Does not appear in the class diagram<br>B. Relates to the class diagram<br>C. exists in the real world and in the class diagram<br>D. Exists in the data world but not in the real model             |
| 6  | When creating a vertical page break.  | A. The active cell must be A1<br>B. The active cell can be anywhere in the worksheet.<br>C. The active cell must be in row 1<br>D. The active cell must be in column A                                  |
| 7  | A computer program that converts an entire program into machine language is called a  | A. Interpreter<br>B. Simulator<br>C. Compiler<br>D. Commander   |
| 8  | How can you access the font size tool on formatting toolbar.  | A. Ctrl + S<br>B. Ctrl + shift + S<br>C. Ctrl + P<br>D. Ctrl + shift + P  |
| 9  | Motorola introduced 32 bit RISC processor called.   | A. MC 88100<br>B. MC 81100<br>C. MC 80100<br>D. MC 81000  |
| 10 | L1 cache memory is places at  | A. On processor<br>B. On mother board<br>C. On memory<br>D. All of these  |
| 11 | The amount of information which can be held at one time in the cache memory is called.  | A. Circle size<br>B. Line size<br>C. Wide line size<br>D. None  |
| 12 | EBCDIC stand for  | A. Extended Binary Coded Decimal interchange code<br>B. Extended Bit Code Decimal interchange code<br>C. Extended Bit case Decimal interchange code<br>D. Extended Binary case Decimal interchange code |
| 13 | The declaration <code>int (*a)[8]</code> : is   | A. An array of pointers<br>B. A pointer to an array<br>C. A pointer to a function<br>D. Function returning pointer  |
|    |   | A. Int  |

|    |  |   |
|----|--|---|
| 14 | The type of the controlling expression of a switch statement can not be of the type            | B. Char<br>C. Float<br>D. Long  |
| 15 | Background color of effects applied on a document is not visible in.                           | A. Web layout view<br>B. Print layout view<br>C. Reading view<br>D. Print preview   |
| 16 | A post implementation review looks into.   | A. Goals achieved vis a vis targets<br>B. User friendliness of the system<br>C. Realization of cos benefits<br>D. Both a ,b and c |
| 17 | If in a computer 16 bits are used to specify address in a RAM, the number of addresses will be | A. 216<br>B. 65.536<br>C. 64 K<br>D. Any above  |
| 18 | Which of the following data type is not seal or data type supported by PHP                     | A. Array<br>B. String<br>C. Float<br>D. Boolean   |
| 19 | the CPU Sends out a _____ signal to indicate that valid data is available on the data bus.     | A. READ<br>B. WRITE<br>C. Both a and b<br>D. None   |
| 20 | Each use case describes how the system reacts to a _____ that occurs to trigger the system.    | A. External entity<br>B. Data flow<br>C. Process<br>D. Event  |