

PPSC Computer Science Full Book test

| Sr | Questions | Answers Choice |
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| 1 | Size of virtual memory depends on | A. Size of data bus B. Size of address bus C. Size of main memory D. None of above |
| 2 | In real time OS, which is most suitable scheduling scheme. | A. Round robin B. FCFS C. Pre-emptive scheduling D. Random scheduling |
| 3 | Dirty bit is used to show | A. Page with corrupted data B. Wrong page in memory C. Page that is modified after being loaded in the cache memory D. Page that is less frequently accessed |
| 4 | Which among following scheduling algorithms give minimum average waiting time. | A. FCFS B. SJF C. Round robin D. On priority |
| 5 | Memory protection is normally done by | A. The processor and the associated hardware B. The operating system C. The compiler D. The user program |
| 6 | Which scheduling policy is best suited for time sharing operating system. | A. Shortest job first B. Round robin C. First come first serve D. Elevator |
| 7 | Belady anomaly occurs in | A. Optimal replacement B. FIFO C. LRU D. Both in FIFO and LRU |
| 8 | Disk scheduling includes deciding | A. Which should be accessed next B. Order in which disk access requests must be serviced C. The physical location of the file D. the logical location of the file |
| 9 | Which one is true. | A. The linkage editor is used to edit program which have to later link together. B. The linkage editor links object module during compiling C. The linkage editor links objects modules and resolves external reference between them before loading. D. None of above |
| 10 | Main function of shared memory is. | A. To use primary memory efficiently B. To do intra process communication C. All of above D. None |
| 11 | Distributed systems should. | A. Meet time constraints B. Aim better resource sharing C. Aim better system utilization D. Aim low system overhead |
| 12 | The communication mode that supports two way traffic but only one direction at a time is. | A. simplex B. Duplex C. Half duplex D. Multiplex |
| 13 | What is the name of the software package that allows people to send electronic mail along a network of computers and work stations. | A. Memory resident package B. Protect management package. C. Data communication package D. Electronic mail package |

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| 14 | Sending a file from your personal computer's primary memory or disk to another computer is called. | A. Uploading B. Down loading C. Logging on D. Hang on |
| 15 | A communication device that combines transmissions from several I/O devices into one line is a. | A. Concentrator B. Modifier C. Multiplexer D. Full duplex line |
| 16 | Most data communications involving telegraph lines use. | A. Simple lines B. Wideband channel C. Narrow band channel D. Dialed service |
| 17 | What is the minimum number of wires needed to send data over a serial communication link layer. | A. 1 B. 2 C. 4 D. 6 |
| 18 | Which data communication method is used to transmit the data over a serial communication link. | A. Simplex B. Half duplex C. Full duplex D. None |
| 19 | When the computer provides the manager with multiple choice of possible answers, the prompting technique is. | A. Question and answer B. Form filling C. Open ended question D. Menu selection |
| 20 | Error detection at a data link level is achieved by | A. Bit stuffing B. Cycle redundancy codes C. Hamming codes D. Equalization |
| 21 | Which generate an interrupt to the microprocessor after a certain interval of time. | A. 8251 B. 8254 C. 8255 D. 8259 |
| 22 | Which is the state of gate signal if normal contains. | A. Low B. High C. Un defined D. None |
| 23 | How many modes in 8254 | A. 2 B. 4 C. 6 D. 8 |
| 24 | 8254 counters can count in the | A. Binary B. Decimal C. Hexadecimal D. Both a and b |
| 25 | 8254 gate of a counter is to either | A. Enable counting B. Disable counting C. Both a and b D. None |
| 26 | 8254 programmable timer counter has. | A. 1 output signal B. 2 out put signal C. 3 out put signal D. 4 out put signal |
| 27 | 8254 programmable timer counter has two inputs signals. | A. CLK B. Gate C. Both a and b D. None |
| 28 | Which used to generate accurate time delays and can be used for other timing application such as a real time clock an event counter a digital one shot a square wave generator and a complex wave form generator. | A. 8251 programmable timer B. 8255 programmable timer C. 8254 programmable timer D. 8259 programmable timer |
| 29 | An externa feedback resistor acts to control the. | A. Gain B. Gate C. Loss D. Profit |
| 30 | Which converter convert binary number into their equivalent voltages. | A. Analogue to analogue B. Analogue to digital C. Digital to digital D. Digital to analogue |