ECAT Computer Science Chapter 5 Boolean Algebra

Sr	Questions	Answers Choice
1	Logical addition refers to operation of	A. OR gate B. AND gate C. NOT gate D. invertr gate
2	Question Image	A. <u>A</u> +<u> B</u> +<u> C</u> + D C. <u>A</u> <u>B</u><u>C</u> <u>D</u> D. A + B + C + D
3	The heart of analog to digital converter (ADC) is	A. comparator B. pulse generator C. voltage source D. current source
4	The logic device that perform Boolean multiplication is.	A. AND gate B. OR gate C. Inverter D. None of these
5	Boolean algebra is.	A. used for arithmetical operation is ALU B. an aid for binary conversion C. useful for error detection and error correction D. used to describe the behavior and structure of logic networks and as an aid in the design of logic system
6	Boolean algebra use which of the following to represent arithmetic quantities.	A. decimal digits B. exponents C. binary bits D. fractions
7	Which of the following function is referred as the complementary.?	A. OR function B. NOT function C. NAND function D. AND function
8	Logical multiplication refers to operation of.	A. OR gate B. AND gate C. NOT gate D. inverter gater
9	According to Idempotent law , x + y = _____	A. 1 B. 0 C. x D. x . x
10	Question Image	A. x + y
11	Question Image	A. x . y B. <u>x + y</u> C. <u>x</u> . <u>y</u> D. x . y
12	Question Image	A. <u>A + B</u> +<u> C + D</u> C. <u>A</u> + <u>B</u> C<u> + D</u>
13	Which of the following gate is two level logic gate.	A. OR gate B. AND gate C. EXCLUSIVE OR gate D. NAND gate
14	The output will be one in case any input is one in the case of.	A. OR gate B. AND gate C. NAND gate D. NOT gate
15	In a three input NAND gate, if all the inputs are 1, the output is.	A. 0 B. 1 C. 3

		D. indeterminate
16	The circuit that is used for parallel to serial conversion is	A. decoder B. encoder C. multiplexer D. demultiplexer
17	If A and B are two 1-bit numbers, what logic gates will be required to test for A=B?	A. NOR gate B. EXCLUSIVE OR gate C. EXCLUSIVE NOT gate D. OR gate
18	According to aborption law x+x.y=	A. x B. y C. 1 + x D. 1 + y
19	Pick up wrong logical expression	
20	Boolean description for the exclusive OR gate for two inputs x and y can be written as.	A. x<u> +</u> y __ B. _x _y C. x.<u> y </u>+<u> x</u>. y D. x . y + x .y