ECAT Computer Science Chapter 2 Digital Computer Fundamentals

Sr	Questions	Answers Choice
1	Network topologies are categorized into the following basic types	A. Bus, Ring B. Star , Tree C. Mesh D. All of the above
2	Wrong Statement.	A. Information stored in RAM can be changed by over writing it B. Information stored in ROM cannot be changed by over writing it C. Information can be stored in any location of RAM D. Computer main memory can be accessed only sequentially
3	The central processing unit comprises of	A. memory ,VDU and printers B. input devices output devices and memory C. software arithmetic and logic unit,and control unit D. software,hardware,and power supply unit
4	Step-by-step instructions that run the computer are.	A. hardware B. documents C. programs D. CPUs
5	The central processing unit comprises of	A. software,hardware,and power supply unit B. software arithmetic and logic unit,and control unit C. memory ,VDU and printers D. input devices output devices and memory
6	Data and instructions are put into primary storage by.	A. memory B. the control unit C. secondary storage D. the ALU
7	IEEE 802.3 is	A. Device Name B. Protocol C. Topology D. None
8	The Media Access Control sub layer resides in which layer	A. Data link B. Physical C. Network D. Transport
9	The heart of the digital computer is	A. control unit B. memory unit C. logic unit D. visual display unit
10	A LAN is a combination of.	A. LAN cables B. Network adapter card C. LAN Application Software D. All of the above
11	During E-time the ALU.	A. examines the instruction B. enters the instruction C. executes the instruction D. elicits the instruction
12	The term "time slicing" need not apply to.	A. On line transacting processing B. Batch processing C. Real time processing D. None of the above
13	Each location in primary storage is assigned a unique.	A. data B. field C. name D. address

14	The heart of a digital computer is.	A. control unit B. memory unit C. logic unit D. visual display unit
15	A CPU has.	A. control unity consisting of program counter and instruction decoder, and arithmetic unit having accumulator B. bubble memory C. visual display unit D. auxiliary storage unit
16	SNA is an example of	A. De Jure Protocol B. De Facto Protocol C. Network organization D. None
17	A network that covers small geographic area or single or group of buildings is called	A. WAN B. LAN C. Bridge D. Gateway
18	The central processing Unit (CPU) comprises of.	A. memory, VDU , and printer B. input device , output device and memory C. store , arithmetic and logic unit and control unit D. software, hardware and power supply unit
19	Computer operations are synchronized by.	A. the CPU clock B. megabytes C. the binary system D. E-time
20	Ethernet bus topologies used following Ethernet cabling	A. Thin Net B. Thick Net C. Bold Net D. a , b