

## ECAT Computer Science Chapter 2 Digital Computer Fundamentals

Sr	Questions	Answers Choice
1	A bus line consists of.	A. registers B. accumulators C. parallel data paths D. machine cycles
2	Which is not associated with the main memory of computer.	A. semiconductor memory B. core memory C. read only memory D. sequential memory
3	Memory is divided into many numerically addressed.	A. logic B. locations C. codes D. cells
4	The process of executing several programs simultaneously by the use of more than one processing unit is called.	A. Multiprogramming B. Time sharing C. Multiprocessing D. Batch processing
5	Ethernet bus topologies used following Ethernet cabling	A. Thin Net B. Thick Net C. Bold Net D. a, b
6	A network that covers small geographic area or single or group of buildings is called	A. WAN B. LAN C. Bridge D. Gateway
7	The complex set of electrical circuitry that executes program instructions is called the.	A. register B. central processing unit C. accumulator D. bus line
8	The heart of a digital computer is.	A. control unit B. memory unit C. logic unit D. visual display unit
9	During E-time the ALU	A. examines the instruction B. enters the instruction C. executes the instruction D. elicts the instruction
10	The CCITT protocol is the same as	A. x.25 B. Y.25 C. C25 D. d25
11	The term "time slicing" need not apply to.	<ul><li>A. On line transacting processing</li><li>B. Batch processing</li><li>C. Real time processing</li><li>D. None of the above</li></ul>
12	Each location in primary storage is assigned a unique.	A. data B. field C. name D. address
13	Another name for a logic chip is.	A. PROM B. memory C. microprocessor D. ROM
14	The business conducted with the help of computer network is called	A. E-commercial  B. E-commerce C. E-comunication D. Nothing
15	What does ISDN stands for?	A. International Service Data Network B. International Service Digital Network C. Integrated Service Data Network

		D. Integrated Service Digital Network
16	The operation of arithmetic logic unit (ALU) is directed by.	A. the ALU itself B. program C. control unit D. memory unit
17	The entire computer system is coordinated by.	A. the ALU B. the accumulator C. the control unit D. arithmetic operators
18	A CPU has	A. control unit consisting of program counter and instruction decoded, and arithmetic unit having accumulator. B. bubble memory C. visual display unit D. auxiliary storage unit
19	OSI model has	A. 11 layers B. 7 layers C. 3 layers D. 10 layers
20	The clock speed of a computer is measured in.	A. megahertz B. kilobytes C. megabytes D. binary digits