

ECAT Computer Science Chapter 2 Digital Computer Fundamentals

Sr	Questions	Answers Choice
1	IEEE 802.3 is	A. Device Name B. Protocol C. Topology D. None
2	The central processing unit comprises of	A. software,hardware, and power supply unit B. software arithmetic and logic unit, and control unit C. memory ,VDU and printers D. input devices output devices and memory
3	The operation of arithmetic logic unit (ALU) is directed by.	A. the ALU itself B. program C. control unit D. memory unit
4	Which is not associated with the main memory of computer.	A. semiconductor memory B. core memory C. read only memory D. sequential memory
5	The complex set of electrical circuitry that executes program instructions is called the.	A. register B. central processing unit C. accumulator D. bus line
6	OSI model has	A. 11 layers B. 7 layers C. 3 layers D. 10 layers
7	Wrong Statement.	A. Information stored in RAM can be changed by over writing it B. Information stored in ROM cannot be changed by over writing it C. Information can be stored in any location of RAM D. Computer main memory can be accessed only sequentially
8	A search engine is	A. A software to search for engines B. A device search for motor engine C. A website that sells products and services D. A website that look through databases for matching criteria
9	The term "time slicing" need not apply to.	A. On line transaction processing B. Batch processing C. Real time processing D. None of the above
10	Which one the common protocols is used for E-mail?	A. FTP B. PPP C. SMTP D. POP
11	A network that covers large area, city, country and World is called	A. LAN B. WAN C. Router D. Bridge
12	Maximum length of co-axial cable used in LAN is	A. 120 meter B. 110 meter C. 100 meter D. 90 meter
13	The business conducted with the help of computer network is called	A. E-commercial B. E-commerce C. E-communication D. Nothing
		A. De Jure Protocol B. De Facto Protocol C. De Minimis Protocol D. De Nihil Protocol

14	SNA is an example of	B. De Facto Protocol C. Network organization D. None
15	The heart of a digital computer is....	A. control unit B. memory unit C. logic unit D. visual display unit
16	The heart of a digital computer is.	A. control unit B. memory unit C. logic unit D. visual display unit
17	At the beginning of the instruction cycle, the contents of the Instruction Register specify.	A. Operand for the instruction being executed B. Instruction itself C. Operand for the instruction to be executed next D. None of above
18	The heart of the digital computer is	A. control unit B. memory unit C. logic unit D. visual display unit
19	A device which connects multiple nodes to the network is	A. A modem B. A Hub C. A repeater D. A router
20	A bootstrap is.	A. a memory device B. a device to support the computer C. a small initialization program to start up a computer D. an error correction technique