

CS-302 Quiz Preparation Virtual University

Sr	Questions	Answers Choice
1	Given the state diagram of an up/down counter, we can find	A. The next state of a given present state B. The previous state of a given present state C. Both the next and previous states of a given state D. The state diagram shows only the inputs/outputs of a given states
2	A positive edge-triggered flip-flop changes its state when	A. Low-to-high transition of clock B. High-to-low transition of clock C. Enable input (EN) is set D. Preset input (PRE) is set
3	The minimum time for which the input signal has to be maintained at the input of flip-flop is called of the flip-flop.	A. Set-up time B. Hold time C. Pulse Interval time D. Pulse Stability time
4	Consider A=1,B=0,C=1. A, B and C represent the input of three bit NAND gate the output of the NAND gate will be	A. Zero B. One C. Undefined D. No output as input is invalid
5	In designing any counter the transition from a current state to the next sate is determined by	A. Current state and inputs B. Only inputs C. Only current state D. current state and outputs
6	The basic building block for a logical circuit is	A. A Flip-Flop B. A Logical Gate C. An Adder D. None of given options
7	FIFO is an acronym for	A. First In, First Out B. Fly in, Fly Out C. Fast in, Fast Out D. None of given options
8	The ABEL symbol for "OR" operation is	A. ! B. & C. # D. \$
9	The 3-variable Karnaugh Map (K-Map) has cells for min or max terms	A. 4 B. 8 C. 12 D. 16
10	The input overrides the input	A. Asynchronous, synchronous B. Synchronous, asynchronous C. Preset input (PRE), Clear input (CLR) D. Clear input (CLR), Preset input (PRE)
11	The binary numbers A = 1100 and B = 1001 are applied to the inputs of a comparator. What are the output levels?	A. A > B = 1, A < B = 0, A < B = 1 B. A > B = 0, A < B = 1, A = B = 0 C. A > B = 1, A < B = 0, A = B = 0 D. A > B = 0, A < B = 1, A = B = 1
12	The output of the expression F=A.B.C will be Logic when A=1, B=0, C=1	A. Undefined B. One C. Zero D. No Output as input is invalid
13	in, all the columns in the same row are either read or written.	A. Sequential Access B. MOS Access C. FAST Mode Page Access D. None of given options

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14	A synchronous decade counter will have flip-flops	A. 3 B. 4 C. 7 D. 10
15	The simplest and most commonly used Decoders are the Decoders	A. (n-1) to (2n-1) B. (n-1) to 2n C. n to 2n-1 D. n to 2n
16	74HC163 has two enable input pins which are and	A. ENP, ENT B. ENI, ENC C. ENP, ENC D. ENT, ENI
17	Demultiplexer is also called.	A. Data selector B. Data router C. Data distributor D. Data encoder
18	DRAM stands for	A. Dynamic RAM B. Data RAM C. Demoduler RAM D. None of given options
19	Caveman number system is Base number system	A. 2 B. 5 C. 10 D. 16
20	LUT is acronym for	A. Look Up Table B. Local User Terminal C. Least Upper Time Period D. None of given options