PPSC Computer Science Topic 17 Microprocessor And Assembly Language

Sr	Questions	Answers Choice
1	Which is called super highway	A. Processor B. Multiplexer C. Backbone bus D. None
2	BIU stand for	A. Bus interface unit B. Bess interface unit C. Both a and b D. None
3	L2 cache memory is places at _____	A. On processor B. On mother board C. On memory D. All of these
4	Customized ROMS are called.	A. Mask ROM B. Flash ROM C. EPROM D. None
5	The external system bus architecture is created using from _____ architecture.	A. Pascal B. Dennis Ritchie C. Charles Babbage D. Von Neumann
6	A nibble can be represented in the from of.	A. Octal digit B. Decimal C. Hexa decimal D. None
7	Which RAM is created using MOS transistors.	A. Dynamic RAM B. Static RAM C. Permanent RAM D. SD RAM
8	The organization of I/O devices create a difference between____	A. Digital computer B. Micro computer C. A and b D. None
9	Which latch is mostly used creating memory register.	A. SR-Latch B. JK- Latch C. D-Latch D. T-Latch
10	RD stand for	A. Read B. Register C. Request D. Real
11	Which process information at a much faster rate than it can retrieve it from memory	A. ALU B. Processor C. Micro procesor D. CPU
12	The standard I/O is also called.	A. isolated I/O B. Parallel I/O C. Both a and b D. None
13	Which are used for port A in 8255 mode 1.	A. PC0-PC2 B. PC3-PC7 C. PC6-PC7 D. PC3-PC5
14	Accumulator based microprocessor example are.	A. Intel 8085 B. Motorola 6809 C. A and B D. None
15	The growth of medium scale integration in.	A. Till 1971 B. Till 1970 C. Till 1972 D. Till 1969

16	How can we make computers work faster.	A. the fetch execute cycle and pipelining B. The assembly C. Both a and b D. None
17	8254 programmable timer counter has two inputs signals.	A. CLK B. Gate C. Both a and b D. None
18	The data in the stack is called.	A. Pushing data B. Pushed C. Pulling D. None
19	a 16 bit address bus can generate _____ address bus.	A. 16 B. 32 C. 36 D. 64
20	WA Stand for	A. Write allocate B. Wrote allocate C. Way allocate D. Word allocate