

PPSC Computer Science Chapter 17 Microprocessor And Assembly Language Online Test

Sr	Questions	Answers Choice
1	How many bit of instruction on our simple computer consist of one	A. 2-bit B. 6-bit C. 12-bit D. None
2	Which formula is used to calculate the number of write stall cycles.	A. Reads * read miss rate* read miss penalty B. write * write miss + Write buffer stalls C. Memory access* cache miss rate * cache miss penalty D. None
3	The act of acquiring an instruction is referred as the the instruction	A. Fetching B. Fetch cycle C. Botha a and b D. None
4	The ddress of a memory is a 20 bit address for the 8086 microprocessor.	A. Physical B. Logical C. Both a and b D. None
5	Which technology using the microprocessor is fabricated on single chip	A. POS B. MOS C. ALU D. ABM
6	Customized ROMS are called.	A. Mask ROM B. Flash ROM C. EPROM D. None
7	PMOS stand for	A. P-Channel metal -oxide semiconductor B. P-Channel memory oxide semiconductor. C. Both a and b D. None
8	is the condiction	A. CD B. IR C. Both a and b D. None
9	NMI stand for	A. Non mask able interrupt B. Non mistake interrupt C. Both a and b D. None
10	Which is the microprocessor launched by intel	A. z-8 B. 8080 C. 8000 D. None
11	A microprocessor retries instructions from	A. Control memory B. Cache memory C. Main memory D. Virtual memory
12	Which is the small amount of highs speed memory used to work directly with the microprocessor	A. Cache B. Case C. Cost D. Coos
13	Which bus transfer singles from the CPU to external device and others that carry singles from external device to the CPU.	A. Control bus B. Data bus C. Address bus D. None
14	Which are used for port B in 8255	A. CPO-PC2 B. PC3-PC7 C. PC6-PC7 D. PC3-CP5

15	Which system communicates with the outside word via the I/O devices interfaced to it.	A. Microprocessor
		B. Micro computer
		C. Digital computer D. All of these
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16	Which is the great importance's in modular programming	A. Stack segment
		B. Array segment
10		C. Queue segment
		D. All of these
	The memory system is said to be effective if the access time of the cache is close to the effective access time of the.	A. ROM
		B. RAM
7		C. HDD
		D. Processor
	LA stand for	A. Load accumulator
18		B. Least accumulator
10		C. Last accumulator
		D. None
	The external device is connected to a pin called the pin on the processor chip.	A. Interrupt
		B. Transfer
19		C. Both
		D. None
		A. MOS
		B. ALU
20	Micro processor is fabricated on single chip using.	C. CPU