

## PPSC Computer Science Topic 17 Microprocessor And Assembly Language

Sr	Questions	Answers Choice
1	Period of fifth generation	A. 1974 -1978 B. 1979-1980 C. 1981-1985 D. 1995 till date
2	Which chip is used for analogue to digital converter.	A. 0809 B. 0808 C. Both a and b D. None
3	How many bit stored by status register	A. 1 bit B. 4 bit C. 6 bit D. 8 bit
4	8254 programmable timer counter has.	A. 1 output signal B. 2 out put signal C. 3 out put signal D. 4 out put signal
5	In which register instruction is decoded prepared and ultimately executed.	A. Instruction register B. Current register C. Both a and b D. None
6	Which of some of popular processor.	A. Motorola 6800 and 6809 B. Intel 8085 C. Zilog Z 80 D. All above
7	Which converter convert binary number into their equivalent voltages.	A. Analogue to analogue B. Analogue to digital C. Digital to digital D. Digital to analogue
8	Which is the part of memory unit	A. Processor memory B. Main memory C. Secondary memory D. All of these
9	The DMA controllers are specials hardware embedded into the chip in modern integrate processor that _____ and _____ to the system.	A. Data transfer B. Arbitrate access C. Both a and b D. None
10	_____ stores the instruction currently being executed.	A. Instruction register B. Current register C. Both a and b D. None
11	INTA stand for	A. Interrupt acknowledge B. Interrupt access C. Interrupt address D. None
12	The register can be divided are.	A. 3 B. 4 C. 5 D. 6
13	LM stand for	A. Least MAR B. Load MAR C. Least MRA D. Load MRA
14	INTR it implies the ___ signal	A. Interrupt request B. Interrupt Right C. Interrupt Rough D. interrupt Reset
15	Which is the microprocessor comprises.	A. Register section B. One or more ALU C. Control unit D. All of these

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16	Which is 16 bit microprocessor .	A. 8088 B. 8086 C. 8085 D. All of these
17	Which programmable timer is used to generate timing signal	A. 8255 B. 8254 C. 8251 D. 8259
18	MOC stand for	A. Memory operation complex B. Micro operation complex C. Memory operation complete D. None
19	How many parts of single address computer instruction.	A. 1 B. 2 C. 3 D. 4
20	HR stand for.	A. Hold request B. Hold read C. Hold register D. Hold resolver

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