

PPSC Computer Science Topic 17 Microprocessor And Assembly Language

Sr	Questions	Answers Choice
1	The time taken for all stages of the assembly line to become active is called the	A. Flow through time B. Clock period C. Through put D. All of these
2	8254 programmable timer counter has.	A. 1 output signal B. 2 out put signal C. 3 out put signal D. 4 out put signal
3	The status register is also called the	A. Condition code register B. Flag register C. a and b D. None
4	8254 counters can count in the	A. Binary B. Decimal C. Hexadecimal D. Both a and b
5	LM stand for	A. Least MAR B. Load MAR C. Least MRA D. Load MRA
6	The index high order bits in the address known as.	A. Tags B. label C. Point D. Location
7	Cache is usually the _____ of memory access by the microprocessor.	A. First level B. Second level C. Third level D. Fourth level
8	LRB stand for	A. Line ready buffers B. Line root buffers C. Line read buffers D. Line right buffers
9	_____ was more common year	A. CRT B. TTL C. Both a and b D. None
10	MEMER and MEMW means	A. Memory read B. Memory write C. Both a and b D. None
11	a 16 bit address bus can generate _____ address bus.	A. 16 B. 32 C. 36 D. 64
12	VAM sand for	A. Valid memory address B. Virtual memory address C. variable memory address D. None
13	DIP stand for	A. Deal inline package B. Dual inline package C. Direct inline package D. Digital inline package
14	Which is used to store critical pieces of data during subroutines and interrupts.	A. Stack B. Queue C. Accumulator D. Data register
15	The standard I/O is also called.	A. isolated I/O B. Parallel I/O C. Both a and b D. None

16	The mode of DMA are	A. Single transfer B. Transfer C. Repeated Sigel transfer D. All of these
17	A central processing unit fabricated on a single chip of semiconductor is called.	A. RAM B. ROM C. Microprocessor D. None
18	EOC stand for	A. End of conversion B. Emphasize of conversion C. End of controller D. None
19	Which is the most commonly measured in term of MIPS previously millions instructing peer second.	A. Microprocesser B. Performance of a microprocessor. C. Assembly line D. None
20	What is the period of fourth generation	A. 1979 - 1980 B. 1981-1995 C. 1995-2000 D. 1974-1980