

## Physics ICS Part 2 Chapter 18 Online MCQ's Test

Sr	Questions	Answers Choice
1	The sensor of light is.	A. Transistor B. LED C. Diode D. Light dependent resistance
2	Which component of the transistor has greater contrition of impurity.	A. Base B. Emitter C. Collector D. Emitter and collector
3	X=A+B is the mathematical notation for.	A. OR gate B. NOR gate C. NAND gate D. AND gate
4	A.C. can be converted into D.C. by	A. An oscillator B. Detector C. An amplifier D. Rectifier
5	NAND gate represented by:	A. X = A. B B. X = A+B C. X= A.B D. X= A+B
6	Which factor does not affect the conductivity of PN-Junction diode.	A. Doping B. Temperature C. Voltage D. Pressure
7	Light emitting diodes are made from semiconductors.	A. Silicon B. Germanium C. Carbon D. Gallium arsenide
8	Rectification is the process of converting.	A. D.C. into A.C. B. A.C. in to D.C. C. Low signal to high D. High signal to low
9	The colour of light emitted by a LED depends on.	A. It forward biased B. Its reverse biased C. Unbiased D. None of these
10	Truth table of logic function.	A. Summarize its output values only     B. Tabulates all its input conditions only     C. Display all its input and output possibility     D. Is not base on logic algebra
11	Photo diode detects.	A. Visible light B. Radio waves C. X rays D. All of them
12	Doping is made comparatively larger in	A. Emitter B. Base C. Collector D. P -type semi conductor
13	The reverse saturation current in a PN junction diode is only due to:	A. Majority carriers B. Minoritycarriers C. Acceptor ions D. Donor ions
14	Which is not fundamental logic gate.	A. NOT B. AND C. OR D. NAND
15	A diode characteristics curve is a plot between	A. Current and resistance     B. Voltage and time     C. Voltage and current

		D. Current and time
6	Transistor was discovered by	A. Young B. Curie C. John Bardeen D. Shale's
7	The open loop gain of the amplifier is order of.	A. 10 <sup>6</sup> B. 10 <sup>5</sup> C. 10 <sup>7</sup> D. 10 <sup>3</sup>
8	The semi conductor diode has the property of	A. Two way conduction B. Zero conduction C. One way conduction D. Amplification
9	OR gate is represented by:	A. X = A+B B. X=A.B C. X=A+B D. X=A.B
0	The p-n junction in which p side is connected to+ive and n-side is -ve the junction is said to be:	A. Neutral B. Forward biased C. Reversed biased D. None of above