Digital Systems and Logic Design

Sr	Questions	Answers Choice
1	In K-map , K stand for..................?	A. Knowldege B. Karnaugh C. Both a and b D. None of these
2	Whihc signals are in discree form?	A. Analog B. Digital C. Both a and b D. None of these
3	In digit system 1 represents?	A. ON B. OFF C. OF D. All of these
4	ALU Stands for..........	A. Arithmetic and Local Unit B. Arithmetic and Legal Unit C. Arithmetic and Logic Unit D. None of these
5	OR operation yields 1 output when at least of........of the input is true.	A. 0 B. 00 C. 1 D. 10
6	What is the dual of Booean expression A.0=0?	A. A+1=1 B. A + 0 = A C. A. 1 = A D. A.O = 0
7	Digital system depends on?	A. 10 B. 00 C. 11 D. None of these
8	Which logic gate outputs true only if both inputs are true?	A. OR gate B. AND gate C. XOR gate D. NOT gat
9	Whcih one of the example of Digital Signal?	A. Binary B. Sound Wave C. 11 D. 00
10	Which signals are in continues form?	A. Digital B. Analog C. Both a and b D. None of these
11	In digit system 0 epresents?	A. OF B. OFF C. ON D. All of these
12	Basic Arthemetic Operation like addition subtractio are performed by?	A. ALU B. CU C. UDP D. All of these
13	What is the decmimal equivalent of the binary number 1101?	A. 13 B. 11 C. 12 D. 14
14	Comparison of two or more values are performed in?	A. CU B. UDP C. LU D. None of these
15	What does DAC represents?	A. Analog Digital Convenient B. Analog Digital Conversation C. Digitla analog conversion D. All of these

16	Which of the following Boolean expressions represents the OR operation	A. A.B B. A+B C. A D. A-B
17	the output of AND Operation is 1 only whenboth inputs are?	A. 10 B. 0 C. 1 D. 00
18	In a half-adder circuit, the carry is generated by which operation?	A. XOR operation B. AND Operation C. OR Operation D. NOT Operation
19	CU stand for...............	A. Circuitry Unit B. Central Unit C. Control Unit D. None of these
20	Bit stand for ....?	A. Binary integer B. Binary Value C. Binary Number D. Binary digit