ECAT Computer Science Chapter 5 Boolean Algebra

Sr	Questions	Answers Choice
1	Which of the following statement is true in the case of AND gate with input A and B.	A. If A and B are applied, there will not be any output B. If neither input is applied, there will be an output C. If one input is applied there will not be any output D. If one input is applied there will be an output
2	NAND gates are preferred over others because these.	A. have lower fabrication area B. can be used to make any gate C. consume least elctronic power D. provide maximum density in a chip
3	The circuit that is used for parallel to serial conversion is	A. decoder B. encoder C. multiplexer D. demultiplexer
4	The output will be one in case any input is one in the case of.	A. OR gate B. AND gate C. NAND gate D. NOT gate
5	According to aborption law x+x.y=	A. x B. y C. 1 + x D. 1 + y
6	The heart of analog to digital converter (ADC) is	A. comparator B. pulse generator C. voltage source D. current source
7	Boolean description for the exclusive OR gate for two inputs x and y can be written as.	A. x<u> +</u> y __ B. _x _y C. x.<u> y </u>+<u> x</u>. y D. x . y + x .y
8	According to Boolean algebra A+A+.....+A is	A. A B. n A C. 0 D. 1
9	Question Image	A. <u>A + B</u> +<u> C + D</u> C. <u>A</u> + <u>B</u> C<u> + D</u>
10	Odd parity of a word can be conveniently tested by.	A. OR gate B. XOR gate C. NOR gate D. NAND gate
11	According to absorption law x. (x+y) =	A. x B. y C. 1+x D. 1+y
12	In Boolean algebra A.A.A.A.A	A. 5A B. A C. A⁵ D. 1
13	Question Image	A. x . y B. <u>x + y</u> C. <u>x</u> . <u>y</u> D. x . y
14	The logic device that perform Boolean multiplication is.	A. AND gate B. OR gate C. Inverter D. None of these
		A. 6

15	An OR gate has 6 input. The number of input words in its truth table are.	B. 32 C. 64 D. 128
16	According to Idempotent law , x + y = _____	A. 1 B. 0 C. x D. x . x
17	Question Image	A. <u>A</u> +<u> B</u> +<u> C</u> + D C. <u>A</u> <u>B</u><u>C</u> <u>D</u> D. A + B + C + D
18	Logical addition refers to operation of	A. OR gate B. AND gate C. NOT gate D. invertr gate
19	The number of inputs to full adder are	A. 1 B. 2 C. 3 D. 4
20	Which of the following operations are used by Boolean algebra.?	A. Boolean addition B. Boolean multiplication C. Boolean complementation D. All of the above